

2A 40V(1.25mm)

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Chip Information

Chip Size	1.25 x 1.25mm
Pad Size	0.99 x 0.99mm
Chip Quantity	6966 pcs/wafer
Scribe Line Width	60um
Passivation	SIN
Wafer Size	5 inch
Top Metallization	Al-Ni-Au (For Solder)

Chip Thickness/Back Metal : See below "Ordering Information"

MAXIMUM RATINGS

Parameter	Symbol	Limit	Unit	Note
Repetitive Peak Reverse Voltage	VRRM	42	V	
Non-Repetitive Peak Reverse Voltage	VRSM		V	
Maximum DC Blocking Voltage	VR		V	
Average Forward Rectified Current	IF(AV)	2	A	
Peak Forward Surge Current	IFSM	50	A	8.3ms Single Half Sine-Wave
Storage and Operating Temperature Range	Tj,TSTG	-65 to +125	degC	

ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Spec Limit	Probe Spec	Typical	Unit	Test Condition
Maximum Forward Voltage	VF1	0.500		0.460	V	IF=2A Ta=25degC
	VF2				V	
	VF3				V	
	VF4				V	
	VF5				V	
Maximum DC Reverse Current	IR1	0.5	0.2	0.03	mA	VR=40V Ta=25degC
	IR2				mA	
	IR3				mA	
	IR4				mA	
Reverse Breakdown Voltage	BV	42	45	55	V	IR=0.45mA
Junction Capacitance	Cj				pF	
Reverse Recovery Time	trr				nS	

Ordering Information

Chip Type	Chip Thickness	Back metal
XJS532	250 +/- 20um	Ti-Ni-Au (For Solder)

Note:
Designed For